

FIGURE 1

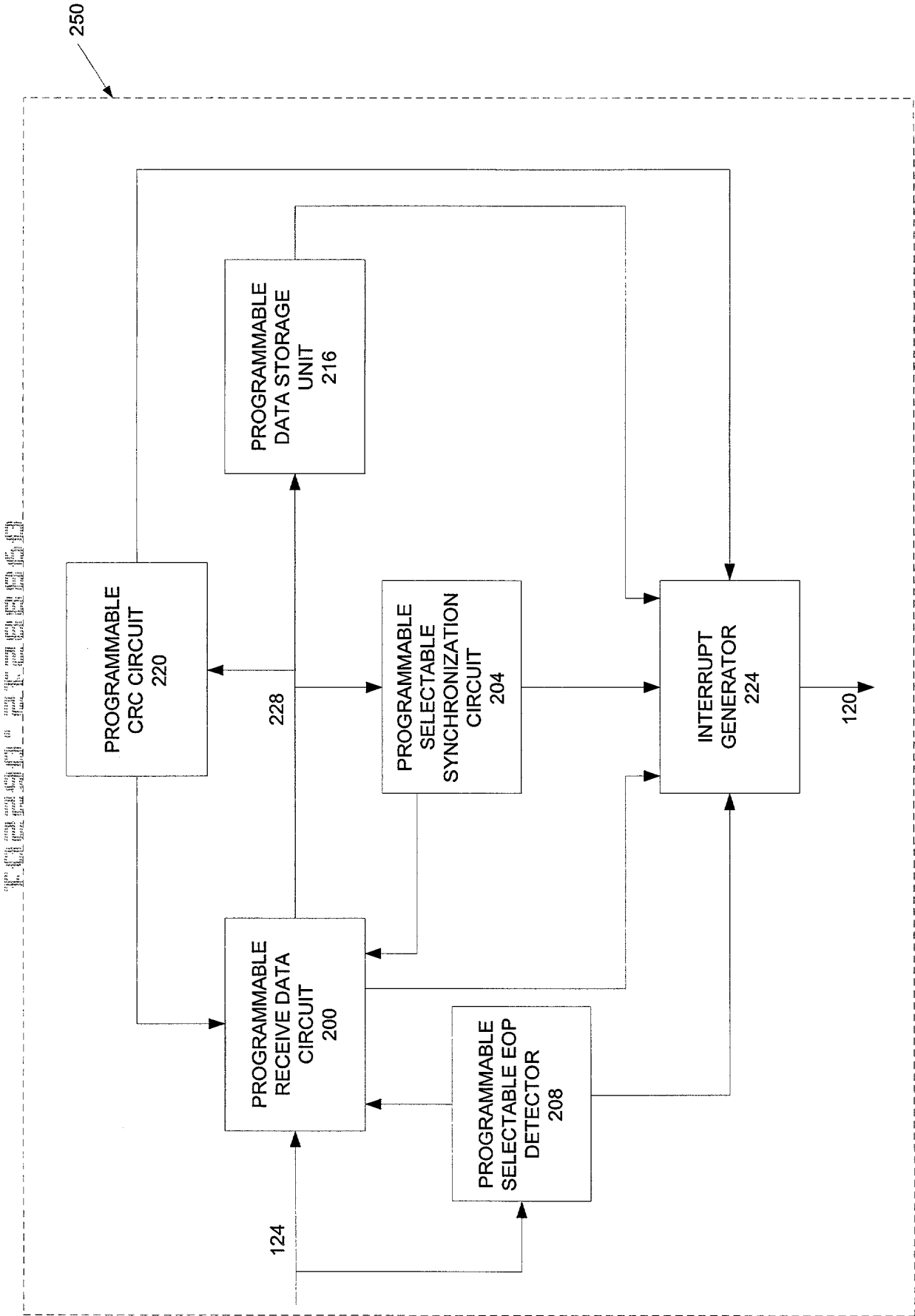


FIGURE 2



FIG. 4

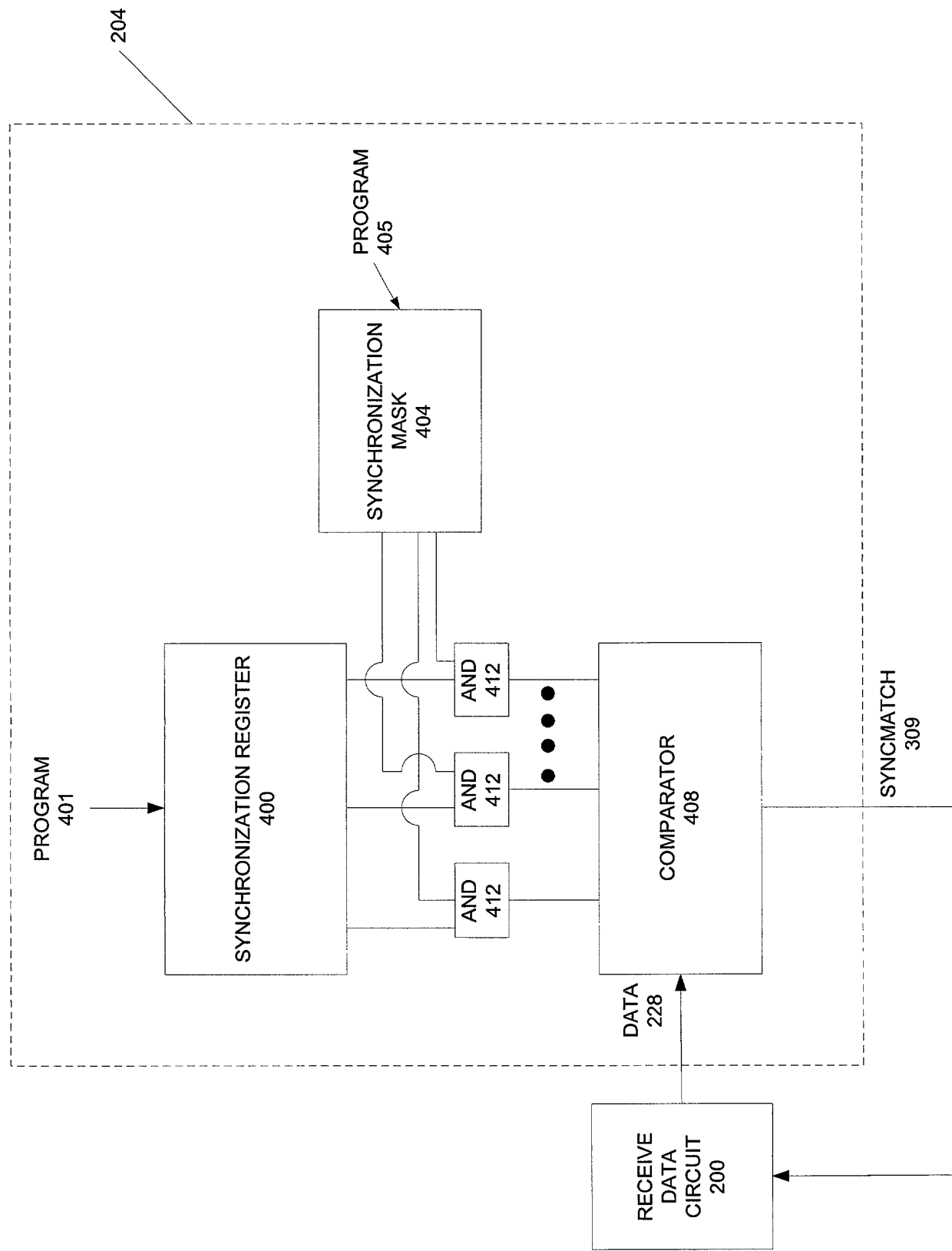


FIGURE 4

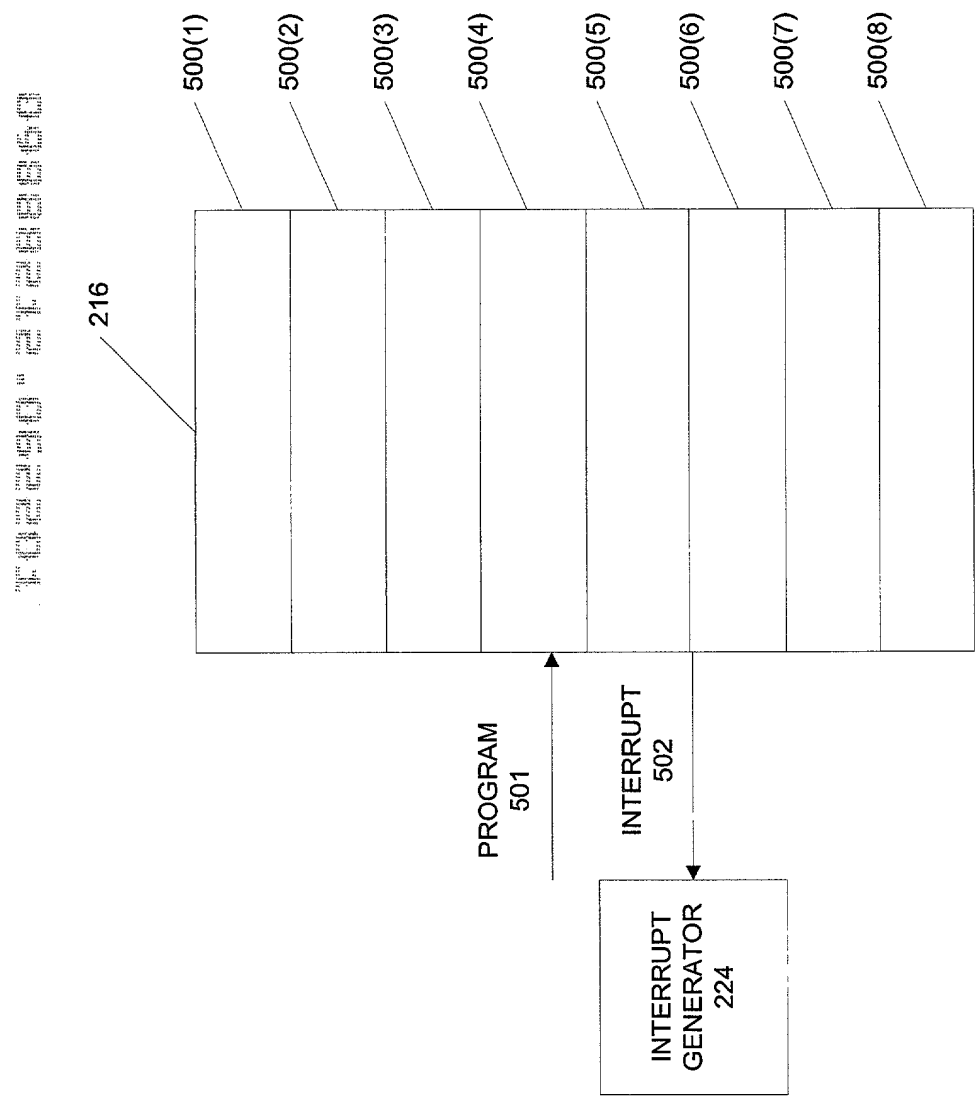


FIGURE 5

FIG. 6 is a block diagram of a Universal Serializer/Deserializer (600) in accordance with the present invention.

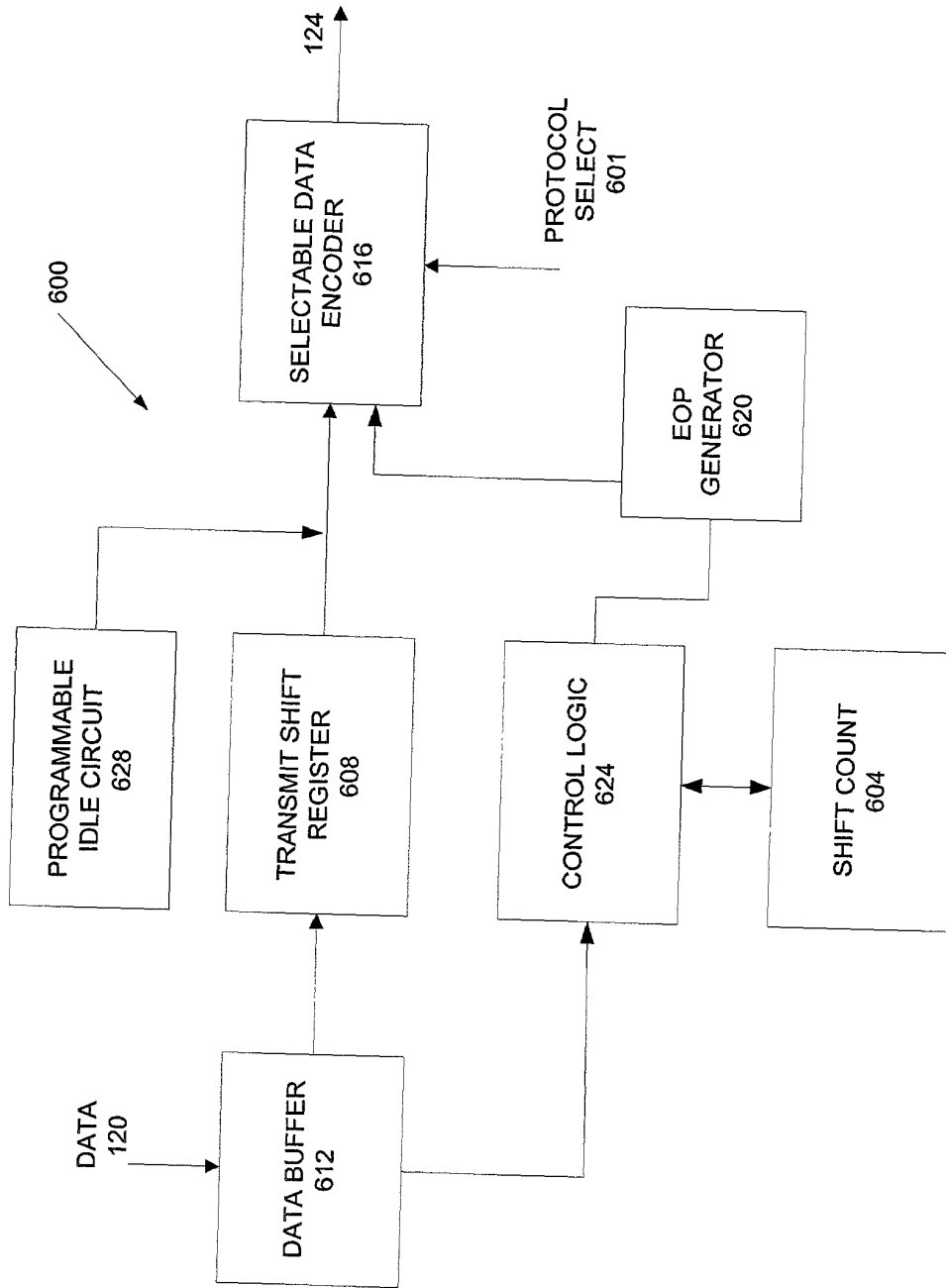


FIGURE 6

FIG. 7

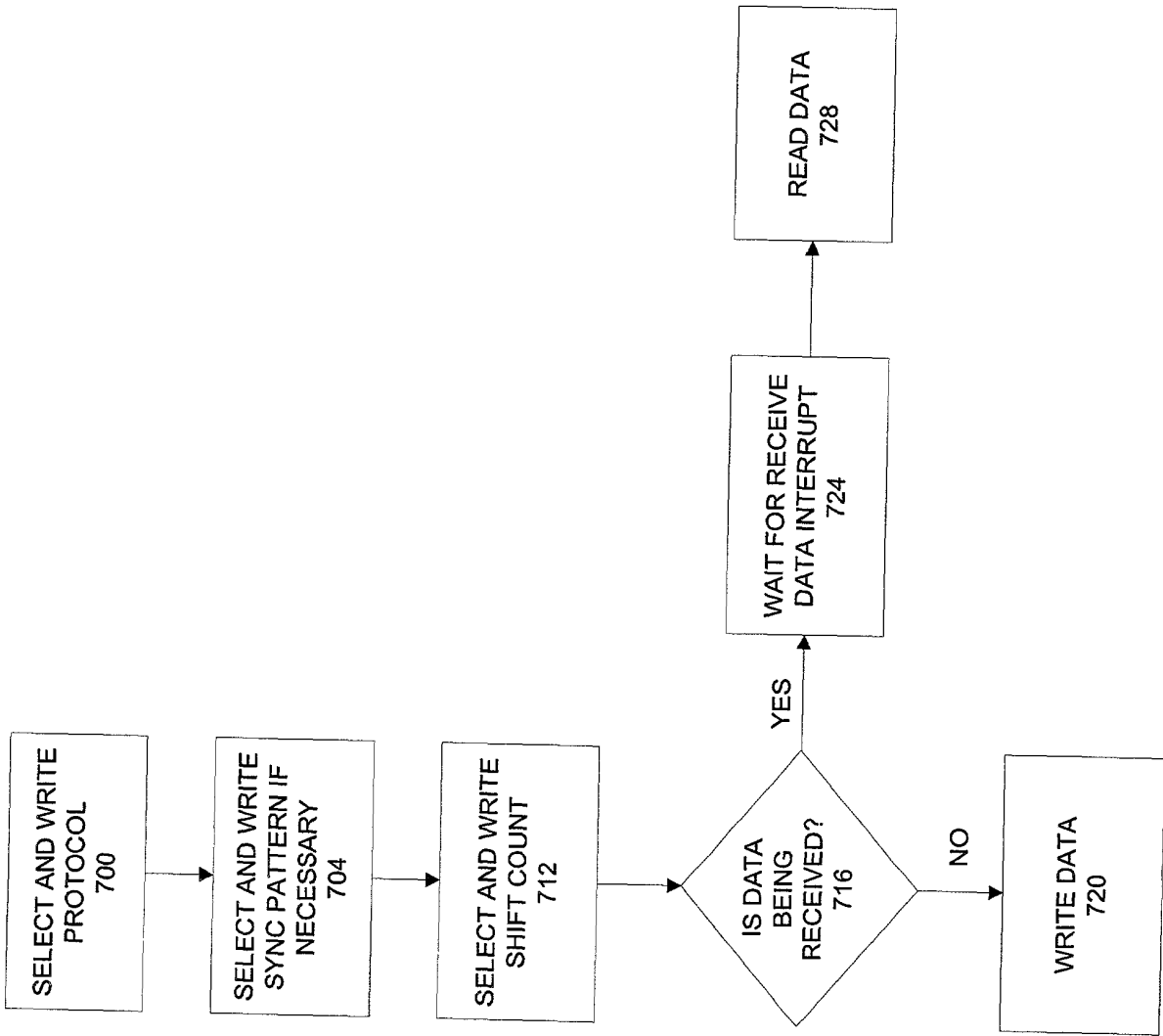


FIGURE 7